

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

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Serial No. 09/670,874

Group Art Unit: 2823

Filed: September 27, 2000

Examiner: Kebede, Brook

For: Semiconductor Integrated Circuit Device

REQUEST FOR APPROVAL OF DRAWING CHANGES

Assistant Commissioner for Patents Washington, D.C. 20231

August 6, 2002

Dear Sir:

Please accept the corrections to Figs. 4 and 5 as indicated in red in the attached paper.

If any fees are required with respect to this paper, please charge Deposit Account No. 50-1770.

Respectfully submitted,

James A. Poulos, III
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Enclosures: Marked up Figs. 4 and 5

Docket No. '874